IN THE CLAIMS:

Please amend claims 1, 20, and 21 as follows:

1. (Currently Amended) An MR coil array comprising:
   (a) a plurality of circuits having a conductive trace mounted on a substrate, each of said circuits adapted to generate a signal;
   (b) means in each said substrate for attaching and aligning at least two of said circuits to one another; and
   (c) wherein a parallel image is ~~provided by~~processed from said circuits.

2. (Original) An MR coil array as defined in claim 1 further comprising means disposed on each of said circuits for pre-amplifying said signal.

3. (Original) An MR coil array as defined in claim 1 wherein said circuits are flexible circuits.

4. (Original) An MR coil array as defined in claim 1 comprising eight of said circuits.

5. (Original) An MR coil array as defined in claim 1 comprising sixteen of said circuits.

6. (Original) An MR coil array as defined in claim 1 comprising thirty-two of said circuits.

7. (Original) An MR coil array as defined in claim 1 wherein said substrate comprises polyimide.

8. (Original) An MR coil array as defined in claim 1 wherein said substrate comprises PTFE.

9. (Original) An MR coil array as defined in claim 1 adapted to operate at field strengths from 0.2 Tesla to 8 Tesla.

10. (Original) An MR coil array as defined in claim 1 wherein at least two of said circuits overlap one another.

11. (Original) An MR coil array as defined in claim 1 wherein at least two of said circuits are interchangeable.

12. (Original) An MR coil array as defined in claim 1 wherein at least two of said circuits are different sizes.

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13. (Original) An MR coil array as defined in claim 1 wherein at least two of said circuits are different shapes.

14. (Original) An MR coil array as defined in claim 1 wherein said means for attaching and aligning at least two of said circuits to one another comprises a plurality of openings formed in said substrates of said circuits.

15. (Original) An MR coil array as defined in claim 2 wherein said means for pre-amplifying comprises matching circuitry.

16. (Original) An MR coil array as defined in claim 2 further comprising a fiber optic interface at an output of said means for pre-amplifying.

17. (Original) An MR coil array as defined in claim 2 wherein said means for pre-amplifying is adapted to output digital signals.

18. (Original) An MR coil array as defined in claim 2 wherein said means for pre-amplifying is adapted to output analog signals.

19. (Original) An MR coil array as defined in claim 2 wherein said means for pre-amplifying is adapted to output both analog and digital signals.

20. (Currently Amended) An MR coil array comprising:
(a) at least eight flexible circuits having a conductive trace mounted on a substrate, each of said flexible circuits adapted to generate a signal;
(b) means in each said substrate for attaching and aligning said flexible circuits to one another;
(c) means disposed on each of said flexible circuits for pre-amplifying said signal; and
(d) wherein a parallel image is ~~provided by~~ processed from said circuits.

21. (Currently Amended) An MR coil module comprising:
(a) a circuit having a conductive trace mounted on a substrate, said circuit adapted to generate a signal; (b) means in said substrate for attaching and aligning at least two of said circuit modules to one another to form a modular MR coil array; and
(c) wherein a parallel image is ~~provided by~~ processed from said circuits.

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22. (Previously Presented) A method of forming a modular MR coil array comprising the steps of:
(a) providing a plurality of circuits having a conductive trace mounted on a substrate, each of said circuits adapted to generate a signal;
(b) providing means in each said substrate for attaching and aligning at least two of said circuits;
(c) attaching and aligning at least two of said circuits to one another to form a modular MR coil array: and
(d) generating a parallel image using said circuits.